



TRANSMITTAL FORM <small>(to be used for all correspondence after initial filing)</small>	Application Number	09/802,017	
	Filing Date	March 8, 2001	
	First Named Inventor	Saulsbury, Ashley	
	Art Unit	2188	
	Examiner Name	Portka, Gary J.	
Total Number of Pages in This Submission	12	Attorney Docket Number	016747-009910US

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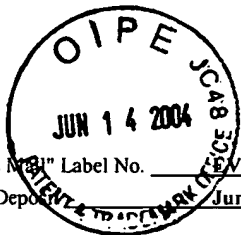
JUN 18 2004

Technology Center 2100

ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Townsend and Townsend and Crew LLP Thomas D. Franklin Reg. No. 43,616
Signature	
Date	June 14, 2004

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**AMENDMENT UNDER 37 CFR 1.116
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Technology Center 2100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ashley Saulsbury, et al.

Application No.: 09/802,017

Filed: March 8, 2001

For: VLIW COMPUTER PROCESSING
ARCHITECTURE WITH ON-CHIP
DRAM USABLE AS PHYSICAL
MEMORY OR CACHE MEMORY

Customer No.: 20350

Confirmation No. 4713

Examiner: Portka, Gary J.

Technology Center/Art Unit: 2188

**AMENDMENT UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2188**

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Sir:

In response to the *Final* Office Action mailed May 12, 2004 on the above-referenced application, please enter the following amendments and remarks:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.